**North SouthUniversity**

Department of Computer Science and Engineering

Quiz-5, Section – 4, Fall-2016

Course No: **CSE 231** Course Title: **Digital Logic Design**

Time: 20 min Full Marks:20

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| 1. | Design a 4-1 multiplexer using 2-4 decoder. | 10 |
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| 2. | Design a full adder circuit using decoder | 10 |